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#### **REMARKS**

The present response is intended to be fully responsive to all points of objection and/or rejection raised by the Examiner and is believed to place the application in condition for allowance. Favorable reconsideration and allowance of the application are respectfully requested.

Applicant asserts that the present invention is new, non-obvious and useful.

#### **Status of Claims**

Claims 1, 3-15, 19-23, 25, 26, 28-31, and 35-37 are pending in the application. Claims 1, 23, 25 and 26 have been amended. Claims 6-15, 29-31, 19-22 and 35-37 have been withdrawn from the application with traverse on a verbal election to a restriction requirement of the Examiner. Claims 2, 16 – 18, 24, 27, and 32-34 have been cancelled without prejudice and disclaimer. In canceling these claims without prejudice, Applicant reserves the right to re-submit these claims in a continuation or divisional application.

#### **Remarks to Amended Claims**

Claims 1, 23, 25 and 26 have been amended. Applicant respectfully asserts that no new matter has been added by the above amendments to claims 1, 23, 25 and 26.

#### **Affirmation of Election**

Applicant affirms the withdrawal of claims 6-15, 19-22, 29-31, and 35-37 pursuant to the election (with traverse and without prejudice to a future divisional, continuation or other prosecution) of Applicant to prosecute the invention of Group I, claims 1-5, 16-18, 23-28 and 32-34, of which claims 16-18, 24, 27, and 32-34 are now canceled.

#### **Telephone Interview**

Applicant thanks Examiner Justin King for his granting and attending a telephone interview to discuss the application and the cited prior art. Attorney of Record Mark Cohen and representative of the Applicant Joel Stein were on the telephone interview with Examiner King. Representatives of the Applicant explained that pages 4 and 5 of the specification as well as Fig. 1 and Fig. 2 of the application indicate that the signals that synchronize the

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interrupts of the processor are issued by structures that are connected to the processor, as such connections are shown on Fig. 1 and Fig. 2. Such structures are the slot timer 22, the interrupt register 24, and the interrupt masking register 28.

#### **CLAIM REJECTIONS**

##### **35 U.S.C. § 102 Rejections**

In the Office Action, the Examiner rejected claims 1-3 and 23-27 under 35 U.S.C. § 102(a), as being anticipated by submitted prior art "On Adaptive DLC Mechanisms for Fixed Broadband Wireless Networks" by Dravopoulos et al. (such article, hereinafter, "Dravopoulos").

Applicant respectfully traverses this rejection of claims 1, 3, 23, 25, 26 and 28 in view of the remarks that follow.

As is well established, in order to successfully assert a *prima facie* case of anticipation, the Examiner must provide a single prior art document that teaches every element and limitation of the claim or claims being rejected.

Dravopoulos describes a system and method for a master scheduler to dictate a schedule of transmissions by a slave scheduler. Dravopoulos does not describe or fairly suggest synchronizing interrupts to a processor with a slot timer of a wireless link synchronization unit connected to said processor, as is required by Applicant's independent claims 1 and 23, as amended. Dravopoulos describes the scheduling of transmissions by a remote device. Such scheduling is not the same as synchronizing an interrupt to a processor by structures connected to the processor.

In view of the above, it is respectfully submitted that independent claims 1 and 23 are not anticipated by Dravopoulos. Accordingly, Applicant respectfully requests that the rejection of claims 1 and 23 under 35 U.S.C. 102(a) be withdrawn.

Furthermore, it is respectfully submitted that independent claims 1 and 23 are patentable, and thus allowable, over the prior art references on record and any combination thereof. In this regard, it is noted that the distinguishing features of independent claims 1 and 23 as discussed above, would not have been obvious at the time the invention was made to a

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person skilled in the art, in view of Dravopoulos, alone or in combination with any other cited references, including the reference discussed below, e.g., with reference to claims 4 and 5.

Claims 3 and 25 depend, directly or indirectly, from independent claims 1 and 23, as amended respectively, and incorporate all the elements of such respective claims. Therefore, it is respectfully submitted that claims 3 and 25, and are patentable, and thus allowable, at least for the reasons set forth above.

In the Office Action, the Examiner rejected claim 26 under 35 U.S.C. § 102(a), as being anticipated by Dravopoulos. Applicant respectfully traverses this rejection of claim 26 in view of the remarks that follow.

Dravopoulos describes a system and method for a master scheduler to dictate schedule of transmissions by a slave scheduler. Dravopoulos does not teach or fairly suggest a state machine to synchronize interrupts to a processor with timing signals from a slot timer of a wireless link synchronization unit connected to the processor, as is required by Applicant's independent claim 26, as amended. Dravopoulos describes the scheduling of transmissions by a remote device. Such scheduling is not the same as a state machine synchronizing an interrupt to a processor by structures connected to the processor.

In view of the above, it is respectfully submitted that independent claim 26, as amended, is not anticipated by Dravopoulos. Accordingly, Applicant respectfully requests that the rejection of claim 26, as amended, under 35 U.S.C. 102(a) be withdrawn.

Furthermore, it is respectfully submitted that independent claim 26, as amended, is patentable, and thus allowable, over the prior art references on record and any combination thereof. In this regard, it is noted that the distinguishing features of independent claim 26 as discussed above, would not have been obvious at the time the invention was made to a person skilled in the art, in view of Dravopoulos, alone or in combination with any other cited references, including the reference discussed below with reference to claims 4 and 5.

#### **35 U.S.C. § 103 Rejections**

In the Office Action, the Examiner rejected claims 4 and 5 under 35 U.S.C. § 103(a), as being unpatentable over Dravopoulos in view of US Patent No. 5,694,613 to Suzuki ("Suzuki").

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According to M.P.E.P. §2142, in order to establish a *prima facie* case of obviousness, three basic criteria must be met. First, there must be some suggestion or motivation, either in the references themselves or in the knowledge generally available to one of ordinary skill in the art, to modify the reference or to combine reference teachings. Second, there must be a reasonable expectation of success. Finally, the prior art references must teach or suggest all the claim limitations. The teaching or suggestion to make the claimed combination and the reasonable expectation of success must both be found in the prior art, and not based on applicant's disclosure.

Without conceding the appropriateness of the combination, Applicant respectfully submits that the combination of Dravopoulos and Suzuki does not meet the requirements of an obviousness rejection, in that the combination fails to teach or suggest all the elements of the claimed invention.

As discussed above, in the remarks to the rejections of independent claims 1 and 23, Dravopoulos does not teach or describe synchronizing an interrupt to a processor by a slot timer or a wireless synchronization unit that is connected to the processor, as is required in Applicant's independent claims 1 and 23, as amended. The Suzuki reference does not cure the deficiency of Dravopoulos because Suzuki does not teach or fairly suggest the limitations of claims 1 and 23 discussed above.

In view of the above, it is respectfully requested that the rejection of claims 4 and 5 under 35 U.S.C. §103(a) be withdrawn.

Claims 2, 16, 17, 18, 24, 27 and 32-34 have been cancelled. The rejection of these claims is therefore moot.

In the Office Action, the Examiner rejected claim 28 under 35 U.S.C. § 103(a), as being unpatentable over Dravopoulos in view of "Operating System Concepts" by Perterson et al. ("Peterson"). Without conceding the appropriateness of the combination, Applicant respectfully submits that the combination of Dravopoulos and Peterson does not meet the requirements of an obviousness rejection, in that the combination fails to teach or suggest all the elements of the claimed invention. As described in the remarks to the rejections of claims 1 and 23, Dravopoulos does not teach or describe synchronizing an interrupt to a processor by a slot timer or a wireless synchronization unit that is connected to the processor, as is required in Applicant's independent claim 23, as amended. Nothing in Peterson cures the

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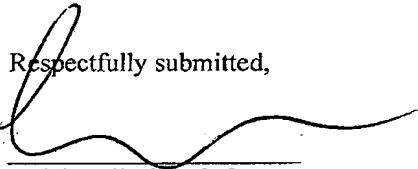
deficiency of Dravopoulos. Accordingly, it is respectfully requested that the rejection of claim 28 under 35 U.S.C. §103(a) be withdrawn.

Conclusion

In view of the foregoing amendments and remarks, the pending claims are deemed to be allowable. Their favorable reconsideration and allowance is respectfully requested.

Should the Examiner have any question or comment as to the form, content or entry of this Amendment, the Examiner is requested to contact the undersigned at the telephone number below. Similarly, if there are any further issues yet to be resolved to advance the prosecution of this application to issue, the Examiner is requested to telephone the undersigned counsel.

Please charge any fees associated with this paper to deposit account No. 50-3355.

  
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